## VyZX

Formal Verification of a Graphical Language

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## The ZX Calculus...

... is a graphical language for reasoning about quantum systems $Z X$ diagrams are open graphs consisting of green "Z" or red " $X$ " "spiders" and "connections" between them


The Z and X spider

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The Z and X spider

Used for compilation, simulation, error correction \& more
Key benefit: Diagrammatic rewrites complete \& more comprehensible than circuits or matrices

## Example: Entanglement (Van de Wetering)



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$$
\left[\begin{array}{lllllllllllllll}
1 & 0 & 0 & 0 & i & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0
\end{array}\right]^{\top}
$$

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## Qubits!

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- $|+\rangle,|-\rangle$ represents the $X$ basis states (transform with hadamard (H))


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- For example, $|\psi\rangle=\alpha|0\rangle+\beta|1\rangle, \alpha, \beta \in \mathbb{C}$.
- The bra notation $\langle\psi|$ is equivalent to $|\psi\rangle^{T}$.
- When measuring a qubit, it collapses to either $|0\rangle$ or $|1\rangle$.
- The probability of measuring $|0\rangle$ is $|\alpha|^{2}$.
- The probability of measuring $|1\rangle$ is $|\beta|^{2}$.
- The sum of probabilities is always 1 : $|\alpha|^{2}+|\beta|^{2}=1$.


## Quantum Circuit Model

- Quantum operations are represented as gates in a circuit model.
- Each gate acts on qubits, changing their states.
- Common gates include Hadamard (H), Pauli-X (X), Pauli-Y (Y), and Pauli-Z (Z).
- Quantum circuits are read from left to right, and gates are applied in sequence.


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$$
\begin{aligned}
& -\pi-\quad \equiv-\quad \text { Z } \\
& -\mathbb{\pi}-\equiv-\sqrt{X} \\
& -\pi-\pi-\bar{Y}- \\
& \square \equiv-
\end{aligned}
$$

## Diagram Semantics



$$
\begin{array}{ccc}
\begin{array}{c}
n \\
|0\rangle \cdots|0\rangle
\end{array} & \mapsto & |0\rangle \cdots|0\rangle \\
n & & m \\
|1\rangle \cdots|1\rangle & \mapsto & e^{i \alpha}|1\rangle \cdots|1\rangle
\end{array}
$$

$$
\left.\begin{array}{c}
\vdots \\
\mathrm{n} \\
\vdots
\end{array}\right)_{\propto}\left(\begin{array}{c} 
\\
\vdots \\
\vdots \\
\vdots
\end{array}=\right.
$$

Diagram Semantics


Diagram Semantics


$$
\mapsto \quad\left[D_{1}\right] \otimes\left[D_{2}\right]
$$

## Rewriting Diagrams







$\alpha$



Spider Fusion, The Hopf Rule, the Bi-pi Rule (Pi-Copy), Bi-hadamard Rule, the Bialgebra rule, and the identity rule

## Spider Fusion

Spider fusion allows us to merge same colored spiders as long as they have one connection, adding their rotations together and connecting any inputs or outputs from the initial two spiders to the final spider.


## Hopf Rule

The hopf rule allows us to disconnect connections between opposite color spiders that come in pairs of two. A consequence of this with spider fusion is that $n$ connections between opposite color spiders can always be considered to be equivalent to $n$ mod 2 connections between those spiders.


## Bi-Pi Rule

The $\mathrm{Bi}-\mathrm{Pi}$ rule allows us to add spiders of opposite colors with rotation $\pi$ to every input and output of a spider and flip the phase.


## Bi-Hadamard Rule

The Bi-Hadamard rule allows us to add H -boxes to every input and output to flip the color of the spider within


## Bialgebra rule

The bialgebra rule is unique in that it is one of the few rules that can introduce or remove swaps.


## Identity Removal

The identity removal rules allow us to remove spiders with $k 2 \pi$ rotations in general.


## Rewriting Diagrams

Fusion can be used between the three connected $X$ spiders here to simplify our diagram.


## Only Connectivity Matters

We can freely move spiders around, as long as their connections and in/outputs remain the same


Example: Preparing a Bell Pair


## Example: Preparing a Bell Pair



## Example: Teleportation (Van de Wetering)



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## Reasoning Tools

Unverified / Fully axiomatic:

- Quantomatic (https://quantomatic.github.io/)
- ZX Calculator (zx.cduck.me)
- Chyp
"Verify" by property testing:
- PyZX (https://github.com/Quantomatic/pyzx)


## ZX Diagrams as string diagrams



## Inductive ZX Diagrams

To define our ZX diagrams, we take these string diagram constructions and add $Z$ and $X$ spiders.


## Semantics

To verify transformations on diagrams, we introduce a system of semantics. Our semantics system will rely on QuantumLib.

$$
\begin{array}{lll}
\text { Z_Spider n m } \alpha & \mapsto & {\left[\begin{array}{ccc}
1 & \cdots & 0 \\
\vdots & \ddots & 0 \\
0 & 0 & e^{i \alpha}
\end{array}\right]} \\
\text { X_Spider n m } \alpha & \mapsto & H^{\otimes m} \times\left[\begin{array}{ccc}
1 & \cdots & 0 \\
\vdots & \ddots & 0 \\
0 & 0 & e^{i \alpha}
\end{array}\right] \times H^{\otimes n}
\end{array}
$$

## More Semantics

$$
\begin{aligned}
\text { Cap } & \mapsto\left[\begin{array}{llll}
1 & 0 & 0 & 1
\end{array}\right] \\
\text { Cup } & \mapsto\left[\begin{array}{llll}
1 & 0 & 0 & 1
\end{array}\right]^{\top} \\
\text { Swap } & \mapsto\left[\begin{array}{llll}
1 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 \\
0 & 1 & 0 & 0 \\
0 & 0 & 0 & 1
\end{array}\right] \\
\text { Empty } & \mapsto[1]
\end{aligned}
$$

Compose zx1 zx2 $\mapsto$ semantics(zx2) $\times$ semantics (zx1)

## Proportionality

Equivalence in ZX is up to constant factor
We define proportionality and use symbol $\propto$ :
$\exists c \neq 0$ : semantics $(z \times 1)=c * \operatorname{semantics}(z \times 2) \Longrightarrow z \times 1 \propto z \times 2$
Allows Coq's rewriting capabilities in our proofs about diagrams

## Why semantics?

- Smaller TCB
- Interoperability


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We can ingest quantum circuits using sqir
Circuit structure is very different
Convert circuit components
Prove equivalence through ground truth

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We can ingest quantum circuits using sqir
Circuit structure is very different
Convert circuit components
Prove equivalence through ground truth

- VyZX can calculate results


## Three Proof Strategies

1. Proof through semantics


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2. Inductive proof


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1. Proof through semantics

2. Inductive proof

3. Diagrammatic proof


## Three Proof Strategies

2. Inductive proof

$$
n: \alpha: m(0 \quad n: \propto+\beta!0
$$

## Other representation



## 2. Inductive proof: Absolute Fusion



The lemma:


Let's induct over $m$

## 2. Inductive proof: Absolute Fusion



Base case:


Solve with matrix semantics!

## 2. Inductive proof: Absolute Fusion

$$
\mathrm{n}: \alpha<\mathrm{m}: 0 \quad \propto \mathrm{n}: \alpha+\beta: 0
$$

Inductive step:


Let's split out nodes to reduce size

## 2. Inductive proof: Absolute Fusion



Idea: Fuse the small spiders. Problem: Association

## 2. Inductive proof: Absolute Fusion



Reassociated diagram


Let's fuse the small spiders

## 2. Inductive proof: Absolute Fusion



Upper spiders are fused


Apply identity rule, remove wires

## 2. Inductive proof: Absolute Fusion



Diagram now corresponds to the IH


## 2. Inductive proof: Absolute Fusion



Qed.


## 3. Diagrammatic proof: Bell pair preparation



The lemma:


Let's first swap colors on the left

## 3. Diagrammatic proof: Bell pair preparation



Let's reassociate!

## 3. Diagrammatic proof: Bell pair preparation



Let's fuse

## 3. Diagrammatic proof: Bell pair preparation



Separate the red node to fuse

## 3. Diagrammatic proof: Bell pair preparation



Let's fuse the red spiders

## 3. Diagrammatic proof: Bell pair preparation



Apply identity rule, remove wires


## 3. Diagrammatic proof: Bell pair preparation



Diagram now corresponds to the a cap



Qed.


## Quantum circuit ingestion from RzQ gate set

$\frac{\mathrm{n}<\mathrm{q}: \mathbb{N}}{\mathrm{H} \mathrm{n}: \text { Circuit } \mathrm{q} q} \quad \frac{\mathrm{n}<\mathrm{q}: \mathbb{N}}{\mathrm{X} n: \text { Circuit } \mathrm{q} \mathrm{q}} \quad \frac{\mathrm{n}<\mathrm{q}: \mathbb{N} \quad \alpha: \mathbb{R}}{\operatorname{Rz}(\alpha) n: \text { Circuit q q }}$
$\frac{\mathrm{c}, \mathrm{n}<\mathrm{q}: \mathbb{N}}{\text { CNOT } \mathrm{c} n: \text { Circuit q q }} \quad \frac{\mathrm{q}: \mathbb{N}}{\text { Compose } c_{1} c_{2}: \text { Circuit q q }}$

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$\frac{\mathrm{n}<\mathrm{q}: \mathbb{N}}{\mathrm{H} \mathrm{n}: \text { Circuit q q }} \quad \frac{\mathrm{n}<\mathrm{q}: \mathbb{N}}{\mathrm{X} n: \text { Circuit } \mathrm{q} \mathrm{q}} \quad \frac{\mathrm{n}<\mathrm{q}: \mathbb{N} \quad \alpha: \mathbb{R}}{\operatorname{Rz}(\alpha) n: \text { Circuit q q }}$
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$$

$$
\mathrm{c}, \mathrm{n}<\mathrm{q}: \mathbb{N}
$$

CNOT c $n$ : Circuit q q
$\frac{\mathrm{q}: \mathbb{N} \quad c_{1}, c_{2}: \text { Circuit q q }}{\text { Compose } c_{1} c_{2}: \text { Circuit q q }}$

$c+1$ $\qquad$

## Discussion

- Any ZX diagram can be expressed
- Multiple ways to encode
- Deal with associativity information
- Dimensionality issues


## How can I verify my graphical language?

- Find underlying categorical structure
- Formally extend structure
- Translate into proof assistant
- Deal with resulting associtativity issues


## Future work

- Restore connection information
- Verify ZX-based compiler
- Prove ZX results


## Summary

- Defined ZX diagrams inductively
- Inspired by string diagrams
- Multiple proof strategies
- ZX calculus is interesting!


## Find VyZX on GitHub

> https://github.com/inQWIRE/VyZX
arXiv
Coming soon...

## References

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